CEZ13C4

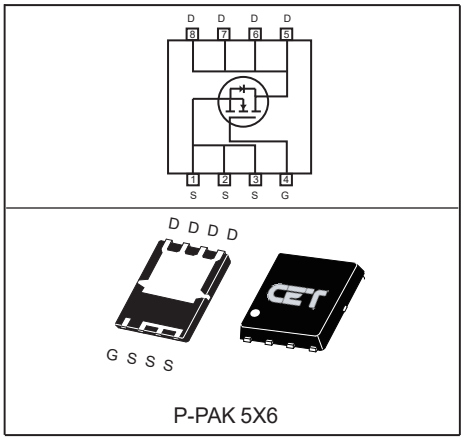
N-Channel Enhancement Mode Field Effect Transistor

PRELIMINARY

FEATURES

V_{DSS}	$R_{DS(ON)}$ typ	I_D	@V_{GS}
45V	1.05mΩ	188A	10V

- High power and current handing capability.
- Reliable and rugged.
- Pb-free lead plating ; RoHS compliant.
- Halogen Free.
- Surface mount Package.



P-PAK 5X6

APPLICATIONS

- DC to DC Converters.
- Battery management systems.
- Motor control.
- Load Switch.

ABSOLUTE MAXIMUM RATINGS

T_C = 25°C unless otherwise noted

Parameter		Symbol	Limit	Units
Drain-Source Voltage		V_{DS}	45	V
Gate-Source Voltage		V_{GS}	±20	V
Drain Current-Continuous	I_D @$R_{\theta JC}$	T_C =25°C	188	A
		T_C =70°C	150	A
	I_D @$R_{\theta JA}$	T_A =25°C	55	A
		T_A =70°C	44	A
Drain Current-Pulsed [a]	I_{DM} @$R_{\theta JC}$	T_C =25°C	752	A
	I_{DM} @$R_{\theta JA}$	T_A =25°C	220	A
Maximum Power Dissipation		P_D	73	W
Operating and Store Temperature Range		T_J,T_{stg}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Limit	Units
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.7	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	20	°C/W

This is preliminary information on a new product in development now
Details are subject to change without notice .

2025.April
http://www.cet-mos.com

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} = 0V, I_D = 250μA	45			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 45V, V_{GS} = 0V			1	μA
Gate Body Leakage Current, Forward	I_{GSSF}	V_{GS} = 20V, V_{DS} = 0V			100	nA
Gate Body Leakage Current, Reverse	I_{GSSR}	V_{GS} = -20V, V_{DS} = 0V			-100	nA
On Characteristics [b]						
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS} = V_{DS}, I_D = 250μA	2		4	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS} = 10V, I_D = 20A		1.05	1.3	mΩ
Gate input resistance	R_g	f=1MHz,open Drain		2.6		Ω
Dynamic Characteristics [c]						
Input Capacitance	C_{iss}	V_{DS} = 20V, V_{GS} = 0V, f = 1.0 MHz		3940		pF
Output Capacitance	C_{oss}			2150		pF
Reverse Transfer Capacitance	C_{rss}			120		pF
Switching Characteristics [c]						
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 20V, I_D = 20A, V_{GS} = 10V, R_{GEN} = 3Ω		33		ns
Turn-On Rise Time	t_r			9		ns
Turn-Off Delay Time	$t_{d(off)}$			48		ns
Turn-Off Fall Time	t_f			14		ns
Total Gate Charge	Q_g	V_{DS} = 20V, I_D = 20A, V_{GS} = 10V		47		nC
Gate-Source Charge	Q_{gs}			14		nC
Gate-Drain Charge	Q_{gd}			12		nC
Drain-Source Diode Characteristics and Maximun Ratings						
Drain-Source Diode Forward Current	I_S				60	A
Drain-Source Diode Forward Voltage [b]	V_{SD}	V_{GS} = 0V, I_S = 2A			1.2	V

Notes :
a.Repetitive Rating : Pulse width limited by maximum junction temperature.
b.Pulse Test : Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
c.Guaranteed by design, not subject to production testing.

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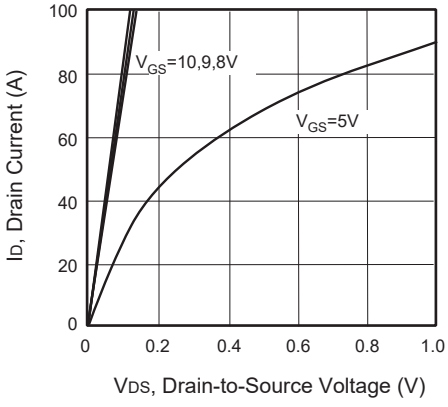


Figure 1. Output Characteristics

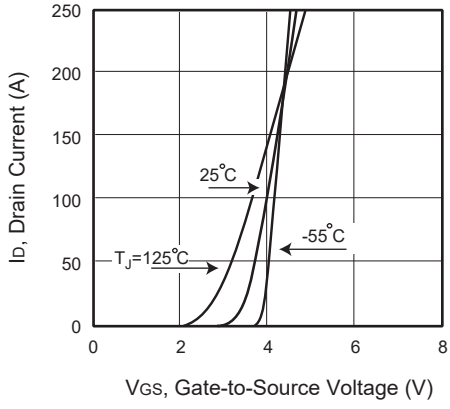


Figure 2. Transfer Characteristics

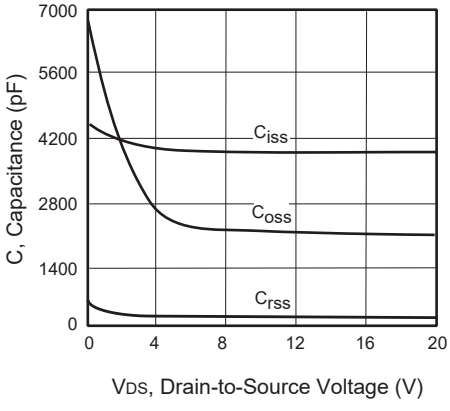


Figure 3. Capacitance

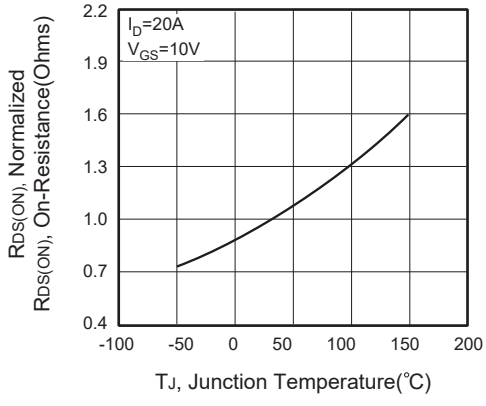


Figure 4. On-Resistance Variation with Temperature

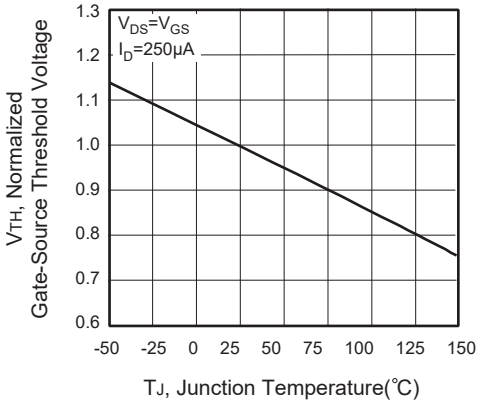


Figure 5. Gate Threshold Variation with Temperature

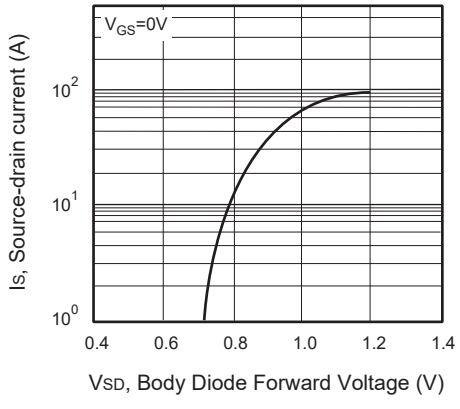


Figure 6. Body Diode Forward Voltage Variation with Source Current

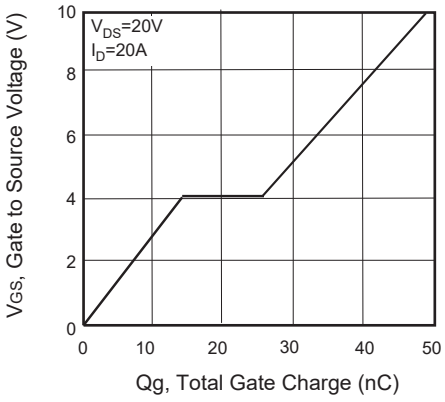


Figure 7. Gate Charge

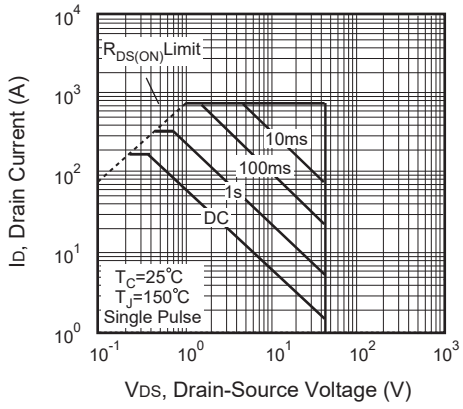


Figure 8. Maximum Safe Operating Area

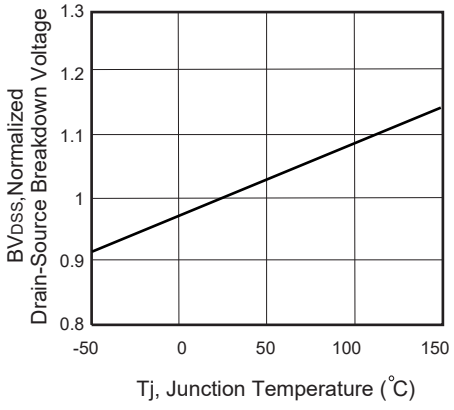


Figure 9. Breakdown Voltage Variation VS Temperature

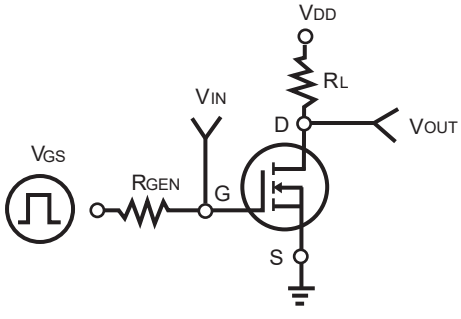


Figure 10. Switching Test Circuit

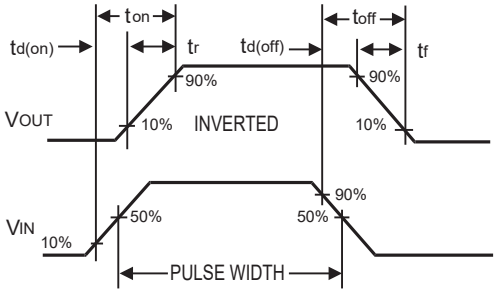


Figure 11. Switching Waveforms

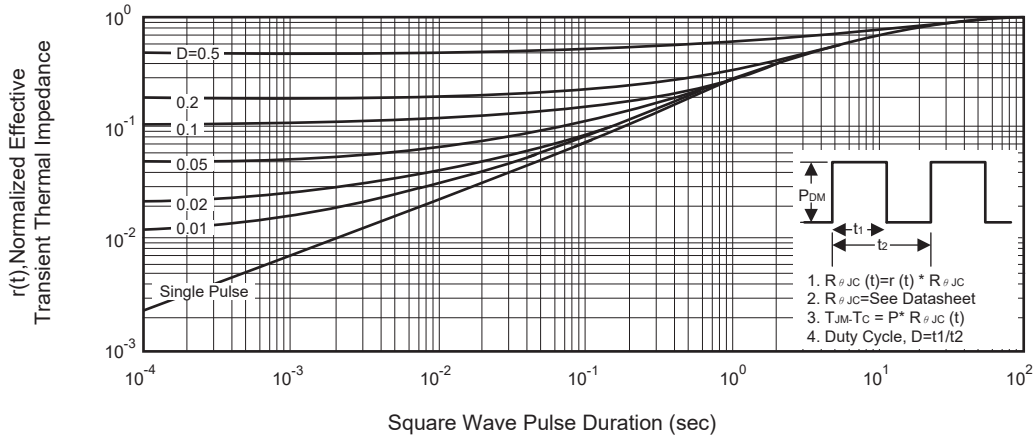
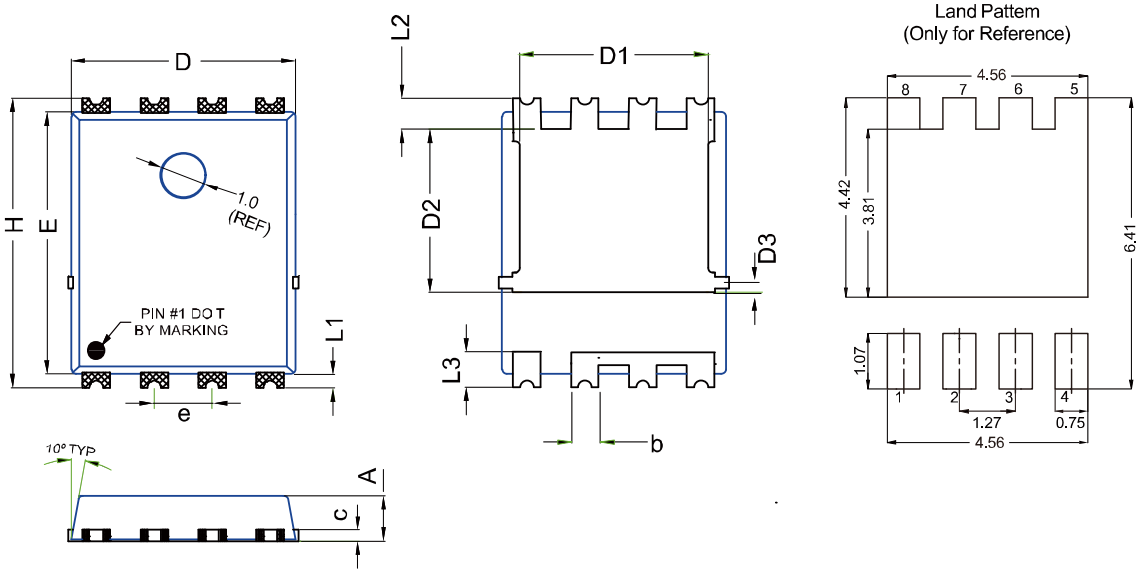


Figure 12. Normalized Thermal Transient Impedance Curve

P-PAK5X6 產品外觀尺寸圖 (Product Outline Dimension)

SINGLE PAD 尺寸圖



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.900	1.100	0.035	0.043
b	0.300	0.500	0.012	0.020
c	0.154	0.354	0.006	0.014
D	4.800	5.200	0.189	0.205
D1	3.800	4.250	0.150	0.167
D2	3.570	3.970	0.141	0.156
D3	0.380	0.850	0.015	0.033
E	5.660	6.060	0.223	0.239
e	1.270 TYP		0.050 TYP	
H	6.250	6.450	0.246	0.254
L1	0.150	0.350	0.006	0.014
L2	0.580	0.780	0.023	0.031
L3	0.680	0.880	0.027	0.035